

Proposal No. 16

(Proposal details for the R&D scheme of USOF)

Subject: Hardware design for FPGA based application

Problem Statement / Challenge title	Development of 400/800Gbps FPGA Card for IPFIX Flow Generation
Challenge brief / definition	IPFIX probe development for handling traffic which is greater than 400 Gbps – 8000Gbps

Format of Response

Companies / organizations / institutions / individuals developing enabling technologies / modules / components / subsystems / products are required to respond in the format provided in Annexure-A, on the DOT website ([link address provided- refer "format of response"](#))